DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first and joint inventor (if plural n which a patent is sought on SEMICONDUCTOR STRUCTURE,"	ames are listed below) of the invention entitled	the subject matter w "METHOD OF MA	mich is ciaii	ned and lot
is attached hereto. was filed on and was amended on _	as Applicatio	n Serial No pplicable).		
I hereby state that I has specification, including the claims,	ave reviewed and under , as amended by any ame	rstand the contents adment referred to abo	of the abo	ve-identified
I acknowledge the duty to me to be material to patentability defined in 37 C.F.R. § 1.56.	of the subject matter o	laimed in this applica	ation, as "m	ateriality is
I hereby claim foreign pri application(s) for patent or invent application listed below designation identified below any foreign appli application, having a filing date be	or's certificate listed belo ng least one country othe cation for patent or inve	w, or under § 365(a) or than the United Stat ntor's certificate, or o	of any PC1 ites of Americ of any PCT i	nternational ca, and have
Prior Foreign Application No.	Country	<u>Filing Date</u> (mm/dd/yy)	<u>Priority</u> <u>Claimed</u>	Cert. copy Attached
N/A				
I hereby claim the benefit listed below.	under 35 U.S.C. § 119(e)	of any United States	provisional a	application(s)
Provisional Application No.	Filing Date (mm/dd/yy)			
N/A				
I hereby claim the benefit or under § 365(c) of any PCT in America, and, insofar as the subject or United States or PCT intern U.S.C. § 112, I acknowledge the patentability of the subject matter 1.56, which became available be international filing date of this ap	nternational application ect matter of each of the cational application in the duty to disclose all infer claimed in this application the filing date of	listed below designate laims of this application manner provided by formation known to ration, as "materiality" the prior application	ing the Unit ion is not dis the first par ne to be ma is defined ir and the nat	sclosed in the cagraph of 35 aterial to the 37 C.F.R. § tional or PCT
Parent Application No.	<u>Filing Date</u> (mm/dd/yy)	Parent Patent No. (if a	applicable) o	r Status
N/A				

The Assignee hereby revokes any previous Powers of Attorney and appoints Andrew D. Fortney, Reg. No. 34,600 and Laura Lee M. Engurasoff, Reg. No. 42,886 of Cypress Semiconductor Corporation; and Kevin L. Daffer, Reg. No. 34,146; B. Noel Kivlin, Reg. No. 33,929; Eric B. Meyertons, Reg. No. 34,876; and Gentry E. Crook, Reg. No. 44,633 of the firm of Conley, Rose & Tayon, as attorney or agent for so long as they remain with such company or firm, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive the Letters Patent.

Please direct all communications to:

Kevin L. Daffer Conley, Rose & Tayon, P.C. P.O. Box 398 Austin, Texas 78767-0398 Phone: (512) 476-1400

I hereby declare that all statements made herein of my own knowledge are true and that all statements made herein on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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